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Washington, DC 20231

7/12/05

Patent No. : 6875649 B2  
Inventor(s) : Oh et al.  
Issued : 4/5/05  
Title : METHODS FOR MANUFACTURING INTEGRATED CIRCUIT DEVICES  
INCLUDING AN ISOLATION REGION DEFINING AN ACTIVE REGION AREA  
Atty.doc./File No.

Request for Certificates of Correction

Consideration has been given to your request for the issuance of a Certificate of Correction, for the above – identified patent under the provisions of CFR 1.322.

Inspection of the application for the patent reveals that col.9 line 9 is printed in accordance with the record. Therefore being no fault on the Patent and Trademark Office, It has no authority to issue a certificate of correction under the provision of 1.322.

In view of the forgoing, your request in this matter, is hereby denied. However, a certificate of correction will be issued to correct the remaining error(s) noted in your request.

Future written correspondence concerning this matter should be filed and directed to  
Decisions & Certificates of Correction Branch.

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